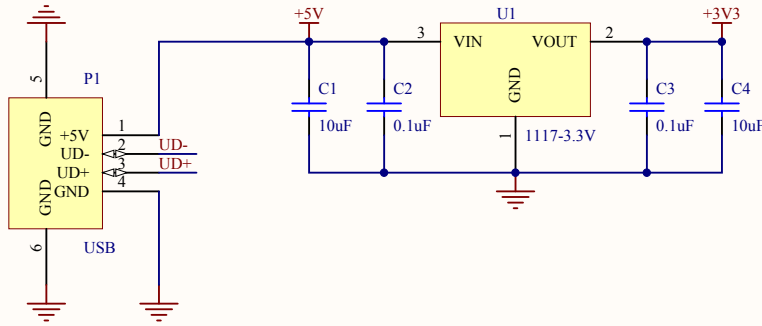
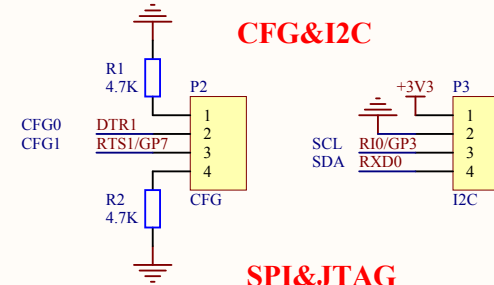


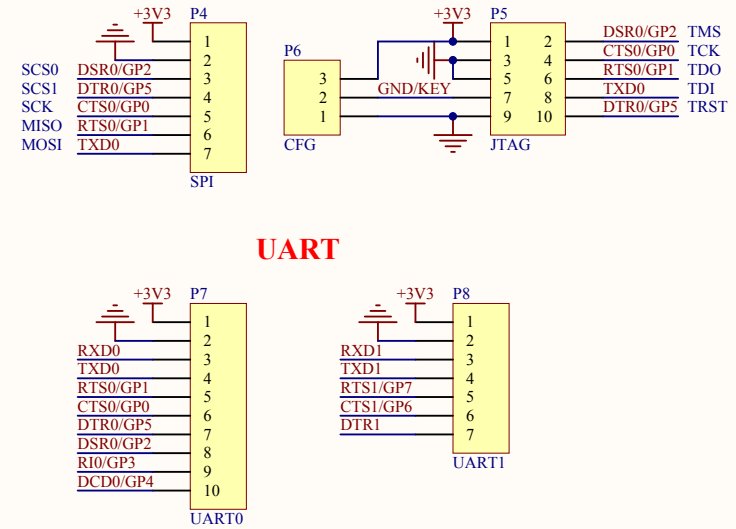
## POWER



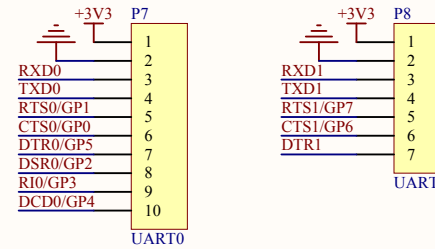
## CFG&I2C



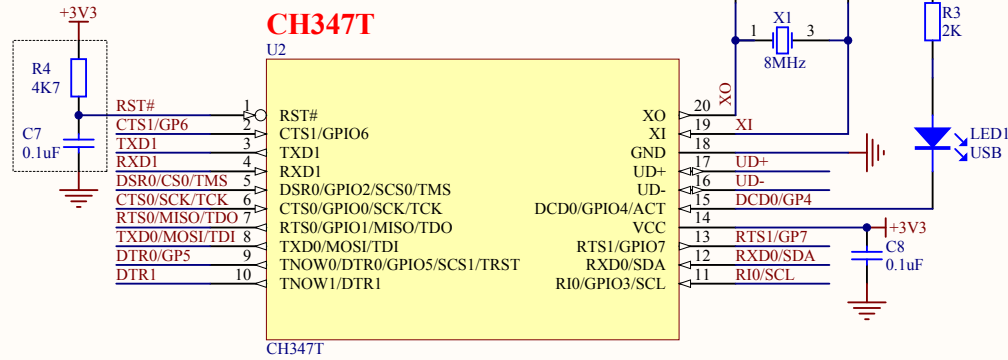
## SPI&JTAG



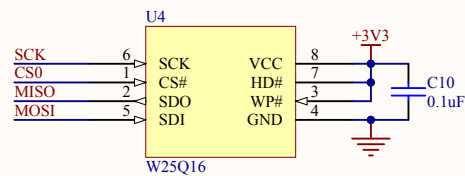
## UART



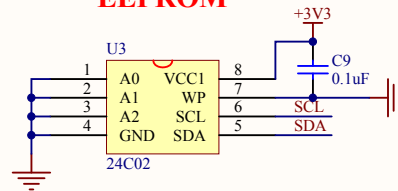
This reset circuit is designed to be optional



## SPI FLASH



## EEPROM



## IO SWITCH

